## LA4708N

## Monolithic Linear IC

 For Car Stereos 20W 2-channel BTL AF Power Amplifier
## Overview

The LA4708N is a BTL two-channel power IC for car audio developed in pursuit of excellent sound quality. Low-region frequency characteristics have been improved through the use of a new NF capacitor-less circuit, and crosstalk which causes "muddy" sound has been reduced by improving both circuit and pattern layout. As a result, the LA4708N provides powerful bass and clear treble. In addition, the LA4708N features on-chip protectors and standby switch.

## Features

- High power : supports total output of $30 \mathrm{~W}+30 \mathrm{~W}\left(\mathrm{~V}_{\mathrm{CC}}=13.2 \mathrm{~V}, \mathrm{THD}=30 \%, \mathrm{R}_{\mathrm{L}}=4 \Omega\right)$
- Supports $\mathrm{R}_{\mathrm{L}}=2 \Omega\left(\mathrm{P}_{\mathrm{O}}=30 \mathrm{~W}\right.$ when $\mathrm{V}_{\mathrm{CC}}=13.2 \mathrm{~V}$, THD $\left.=10 \%\right)$
- Designed for excellent sound quality ( $\mathrm{f}_{\mathrm{L}}<10 \mathrm{~Hz}, \mathrm{f}_{\mathrm{H}}=130 \mathrm{kHz}$ )
- NF capacitor-less
- Any on time settable by external capacitor
- Less pop noise
- Standby switch circuit on chip (microprocessor supported)
- Various protectors on chip (output-to-ground short/output-to- ${ }_{\text {CC }}$ short/load short/overvoltage/thermal shutdown circuit)
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## Specifications

Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Maximum supply voltage | $\mathrm{V}_{\text {CC }} \max 1$ | No signal, $\mathrm{t}=60 \mathrm{~s}$ | 24 | V |
|  | $\mathrm{V}_{\text {CC }} \max 2$ |  | 16 | V |
| Surge supply voltage | $\mathrm{V}_{\text {CC }}$ surge | $\mathrm{t} \leq 0.2 \mathrm{~s}$, single giant pulse | 50 | V |
| Maximum output current | Io peak | Per channel | 4.5 | A |
| Allowable power dissipation | Pd max | Arbitrarily large heat sink | 37.5 | W |
| Operating temperature | Topr |  | -35 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg |  | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |

* Set $\mathrm{V}_{\mathrm{CC}}, \mathrm{R}_{\mathrm{L}}$ in a range that does not exceed $\mathrm{Pd} \max =37.5 \mathrm{~W}$

Operating Conditions at Ta $=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Recommended supply voltage | $\mathrm{V}_{\mathrm{CC}}$ |  | 13.2 | V |
| Operating voltage range | $\mathrm{V}_{\text {CC }}$ op | Range where Pd max is not exceeded | 9 to 16 | V |
| Recommended load resistance | $\mathrm{R}_{\mathrm{L}}$ |  | 4 | $\Omega$ |
| Recommended load resistance range | $\mathrm{R}_{\mathrm{L}}$ op |  | 2 to 4 | $\Omega$ |

Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=13.2 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=4 \Omega, \mathrm{f}=1 \mathrm{kHz}, \mathrm{Rg}=600 \Omega$

| Parameter | Symbol | Conditions | min | typ | max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Quiescent current | ${ }^{\text {I CCO }}$ |  | 70 | 150 | 250 | mA |
| Standby current | Ist |  |  | 10 | 60 | $\mu \mathrm{A}$ |
| Voltage gain | VG |  | 38 | 40 | 42 | dB |
| Total harmonic distortion | THD | $\mathrm{P}_{\mathrm{O}}=2 \mathrm{~W}$ |  | 0.07 | 0.4 | \% |
| Output power | $\mathrm{P}_{\mathrm{O}} 1$ | THD $=10 \%$ | 16 | 20 |  | W |
|  | $\mathrm{PO}^{2}$ | THD $=10 \%, \mathrm{~V}_{\mathrm{CC}}=14.4 \mathrm{~V}$ |  | 24 |  | w |
|  | $\mathrm{P}_{\mathrm{O}} 3$ | THD $=10 \%, \mathrm{R}_{\mathrm{L}}=2 \Omega$ |  | 30 |  | W |
| Output offset voltage | $\mathrm{V}_{\mathrm{N}}$ offset | $\mathrm{Rg}=0$ | -300 |  | +300 | mV |
| Output noise voltage | $\mathrm{V}_{\mathrm{NO}}$ | $\mathrm{Rg}=0$, B.P.F. $=20 \mathrm{~Hz}$ to 20 kHz |  | 0.1 | 0.5 | mVrms |
| Ripple rejection ratio | SVRR | $\mathrm{Rg}=0, \mathrm{f}_{\mathrm{R}}=100 \mathrm{~Hz}, \mathrm{~V}_{\mathrm{R}}=0 \mathrm{dBm}$ | 40 | 50 |  | dB |
| Channel separation | CHsep | $\mathrm{Rg}=10 \mathrm{k} \Omega, \mathrm{V}_{\mathrm{O}}=0 \mathrm{dBm}$ | 50 | 60 |  | dB |
| Input resistance | $\mathrm{r}_{\mathrm{i}}$ |  | 21 | 30 | 39 | $\mathrm{k} \Omega$ |
| Standby pin applied voltaga | Vst | Amp on, applied through 10k $\Omega$ | 2.5 |  | $\mathrm{V}_{\mathrm{CC}}$ | V |

## Package Dimensions

unit: mm (typ)
3109A



## Block Diagram



## Each Pin Voltage

$\mathrm{V}_{\mathrm{CC}}=13.2 \mathrm{~V}, 5 \mathrm{~V}$ applied through STBY $=10 \mathrm{k} \Omega, \mathrm{R}_{\mathrm{L}}=4 \Omega, \mathrm{Rg}=0$

| Pin No. | 1 | 2 | 3 | 4 | 5 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Name |  | $I N 1$ | DC | Pre-GND | STBY |
| Pin voltage $(V)$ | 0.29 | 1.58 | 6.55 | 0 | ON TIME |


| Pin No. | 7 | 8 | 9 | 10 | 11 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Name | IN2 | POP | - | N.C | $V_{\text {CC }}{ }^{2}$ |
| Pin voltage (V) | 1.58 | 2.08 | 0.29 | 0 | 12.2 |


| Pin No. | 13 | 14 | 15 | 16 | 17 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Name | PWR-GND 2 | +OUT 2 | +OUT 1 | PWR-GND 1 | - OUT 1 |
| Pin voltage (V) | 0 | 6.5 | 6.5 | 0 | V $_{\text {CC }} 18$ |

## Sample Print Pattern



## Description of External Components

| C1, C4 | Input capacitors | $2.2 \mu \mathrm{~F}$ is recommended. f can be varied by C1, C4 capacitances to adjust the bass range. |
| :--- | :--- | :--- |
| C2 | Decoupling capacitor <br> (ripple filter) | Amplifier on time setting <br> capacitor |
| C3 | Power supply capacitor | Approximately 0.8 second for $33 \mu \mathrm{~F}$. Since the on time is proportional to this capacitance, it can <br> be set as desired by varying this capacitance. (Refer to the characteristics curve.) |
| C5 | Oscillation blocking capacitors | Use polyester film capacitors (Mylar capacitors) with good temperature characteristics. (R2, R3, <br> R4, and R5 used jointly.) Since stability may be affected slightly by the pattern layout, etc., <br> $0.1 \mu \mathrm{~F}$ or more is recommended. |
| C6, C7, C8, C9 | $10 \mathrm{k} \Omega$ is recommended (when the applied voltage for the standby switch is 2.5 V to 13.2 V ). This <br> resistor cannot be removed. |  |
| R1 | Standby switch current <br> limiting resistor |  |

## Features of IC Inside and Usage Notes

## Standby function

- Pin 5 is the standby switch pin. The amplifier is turned on by applying approximately 2.5 V or more to this pin through an external resistor (R1).
- If voltage in excess of 13.2 V is to be applied to the standby switch, calculate the value of R1 using the following formula so that the current flowing into pin 5 is $500 \mu \mathrm{~A}$ or less:

$$
\mathrm{R} 1=\frac{\text { Applied voltage }-1.4 \mathrm{~V}}{500 \mu \mathrm{~A}}-10 \mathrm{k} \Omega
$$

## Mute function

- Pin 6 is the connector for the capacitor that determines the on time in order to prevent pop noise. By grounding this pin, the amplifier can implement mute operation. In this case, the recovery time depends on C3.


## How to reduce pop noise

- Although the LA4708N reduces pop noise, an electrolytic capacitor of between 0.47 and $2.2 \mu \mathrm{~F}$ can be connected between pin 8 and the pre-GND to further reduce pop noise that occurs when power supply is turned on/off (standby switch on/off). The larger the capacitance, the lower the frequency of pop noise, and it is barely audible, but sound residue of the sound signal is liable to linger when power is turned off. Pin 8 is the bias pin for the output amplifier and normally is left open.


## Protectors

- In an output-to-ground and output-to-VCC short protector system configuration, if a DC resistor is connected between amplifier output pin and GND, the protector may operate, causing the amplifier not to start operating. Therefore, as a general rule, no DC resistor should be connected between amplifier output pin and GND.
- In order to prevent damage or degradation which may be caused by abnormally heated IC, the LA4708N has a thermal shutdown protector. Accordingly, if the IC junction temperature (Tj) climbs to around 170 to $180^{\circ} \mathrm{C}$ due to inadequate heat dissipation, the thermal shutdown protector will operate to control the output gradually into attenuation.
- Also be fully careful of handling other protectors built in the LA4708N.


## Miscellaneous

- Since pins 1 and 9, which are unused, are connected internally, they must be left open.
- Pin 10 is an NC pin (no internal connection).







THD - f















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